

Chapter 16: Noise, Errors, and Hardware Constraints

This section is already in the book plan, but it has not been written fully yet. The book owner can press Generate section to write this part with the language model connected to TheoryTrace.

Section plan:

Explains decoherence, gate errors, readout errors, limited connectivity, shot noise, barren plateaus, and how these affect optimization performance. Introduces error mitigation and realistic benchmarking.

References

References will be added when this section is generated.

Document information

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