Chapter 21: Limits, Noise, and Real Hardware

This section is already in the book plan, but it has not been written fully yet. The book owner can press Generate section to write this part with the language model connected to TheoryTrace.

Section plan:

Explains the gap between the abstract algorithm and physical implementation. The chapter covers qubit counts, circuit depth, gate errors, decoherence, fault tolerance, quantum error correction, and why large-scale factoring requires error-corrected quantum computers.

References

References will be added when this section is generated.

Document information

Chapter 21: Limits, Noise, and Real Hardware

Project	Shor's Algorithm from First Principles
Document	Document 1.25
Author	mujirin
Verifier	Not verified
Downloaded	July 04, 2026 22:21 KST
Status	Working
Document link	https://www.theorytrace.com/projects/shors-algorithm-from-first-principles/documents/-chapter-21-limits-noise-and-real-hardware/